

I7565H1H2 Linux Software Manual

User Manual

Warranty

All products manufactured by ICP DAS are warranted against defective materials for a period of one year from the date of delivery to the original purchaser.

Warning

ICP DAS assume no liability for damages consequent to the use of this product. ICP DAS reserves the right to change this manual at any time without notice. The information furnished by ICP DAS is believed to be accurate and reliable. However, no responsibility is assumed by ICP DAS for its use, nor for any infringements of patents or other rights of third parties resulting from its use.

Copyright

Copyright 2015 by ICP DAS. All rights are reserved.

Trademark

The names used for identification only may be registered trademarks of their respective companies.

Tables of Contents

1. i-7565-H1/H2 Linux Driver Installation	3
1.1 LINUX DRIVER INSTALLING PROCEDURE.....	3
1.2 LINUX DRIVER UNINSTALLING PROCEDURE.....	4
2. i-7565-H1/H2 Static Library Function Description	5
2.1 TABLE OF ERRORCODE AND ERRORSTRING	6
2.2 FUNCTION DESCRIPTIONS	6
2.3 INIT FUNCTIONS	7
2.3.1 <i>VCI_OpenCAN</i>	7
2.3.2 <i>VCI_CloseCAN</i>	8
2.4 MODULE CONFIGUREURE FUNCTIONS	8
2.4.1 <i>VCI_Set_CANFID</i>	8
2.4.2 <i>VCI_Get_CANFID</i>	9
2.4.3 <i>VCI_Clr_CANFID</i>	10
2.4.4 <i>VCI_Get_CANStatus</i>	10
2.4.5 <i>VCI_Clr_BufOverflowLED</i>	11
2.4.6 <i>VCI_Get_MODInfo</i>	12
2.4.7 <i>VCO_Rst_MOD</i>	12
2.5 COMMUNICATION FUNCTIONS.....	12
2.5.1 <i>VCI_SendCANMsg</i>	13
2.5.2 <i>VCI_RecvCANMsg</i>	13
2.5.3 <i>VCI_EnableHWCyclicTxMsg</i>	14
2.5.4 <i>VCI_DisableHWCyclicTxMsg</i>	15
2.6 SOFTWARE BUFFER FUNCTIONS	16
2.6.1 <i>VCI_Get_RxMsgCnt</i>	16
2.6.2 <i>VCI_Get_RxMsgBufIsFull</i>	17
2.6.3 <i>VCI_Clr_RxMsgBuf</i>	17
2.7 OTHER FUNCTIONS.....	17
2.7.1 <i>VCI_Get_Library_Version</i>	18
3. i-7565-H1/H2 Demo Programs For Linux	19
3.1 DEMO NAME “i7565H1H2_A” & “i7565H1H2_so”	20
3.2 DEMO CODE “i7565H1H2_A”	20

1. i-7565-H1/H2 Linux Driver Installation

The I-7565-H1/H2 can be used in linux. For Linux O.S, the recommended installation and uninstall steps are given in Sec 1.1 ~ 1.2

1.1 Linux Driver Installing Procedure

(1) LinPAC-8x41(arm)

- Type below command to install linux driver

I-7565-H1 module:

```
#cd /lib/modules/2.6.19  
#insmod usbserial vendor=0x1b5c product=0x0201
```

I-7565-H2 module:

```
#cd /lib/modules/2.6.19  
#insmod usbserial vendor=0x1b5c product=0x0202
```

- Type command “dmesg” to check I-7565-H1/H2 device file(please refer to Figure 1-1)

```
#dmesg
```

(2) LinPAC-8x81 or Linux PC(x86)

Execute script “I7565H1H2_install” to install I7565-H1/H2 driver.

```
./I7565H1H2_install [module name]
```

Example: Install I7565-H1 module driver

```
./I7565H1H2_install 1
```

- Type command “dmesg” to check I-7565-H1/H2 device file(please refer to Figure 1-1)

#dmesg

```
usb 1-1: generic converter now attached to ttyUSB0 i-7565-H1/H2 linux device file  
usbcore: registered new driver usbserial_generic  
drivers/usb/serial/usb-serial.c: USB Serial Driver core
```

Figure 1-1

1.2 Linux Driver Uninstalling Procedure

(1) LinPAC-8x41(arm)

- Type below command to remove i-7565-H1/H2 linux driver.

```
#rmmod usbserial
```

(2) LinPAC-8x81 or Linux PC(x86)

Execute script "I7565H1H2_install" to install I7565-H1/H2 driver.

```
./I7565H1H2_install remove
```

2. i-7565-H1/H2 Static Library Function Description

The static library is the collection of function calls of the i-7565-H1/H2 for linux kernel 2.6.x system. The application structure is presented as below figure “Figure 2-1”. The user application program developed by C (C++) language can call library “libI7565H1H2.a” for LinPAC-8x81(or x86 linux PC) or “libI7565H1H2_arm.a” for LinPAC-8x41 in user mode. And then static library will call the module command to access the hardware system.

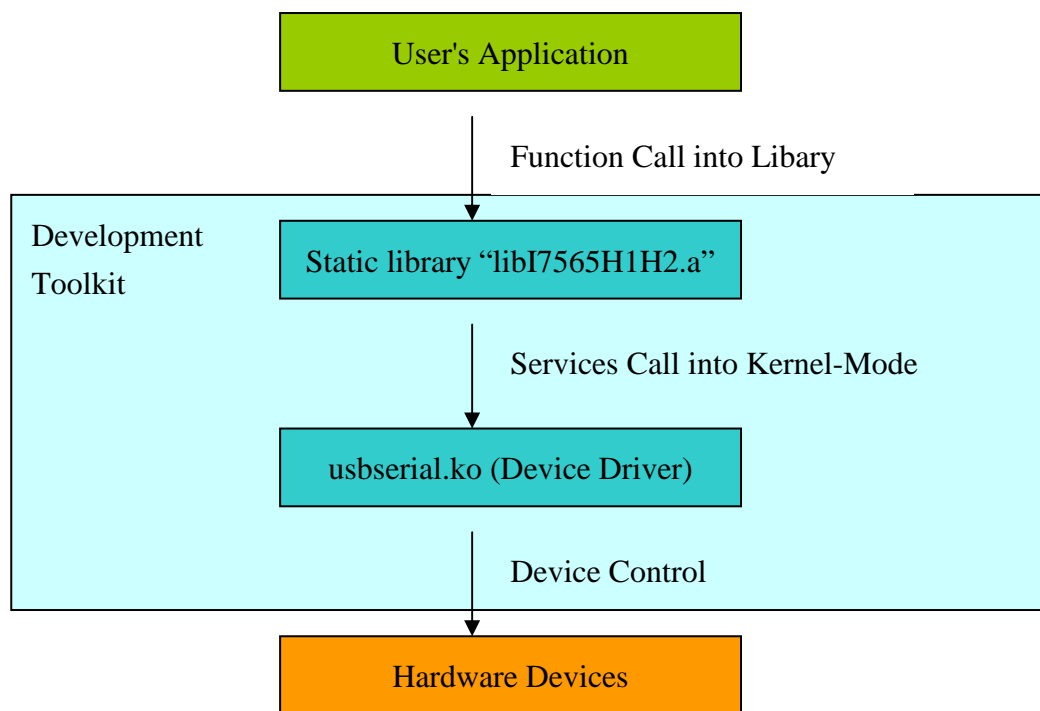


Figure 2-1

2.1 Table of ErrorCode and ErrorString

Table 2.1

Error Code	Error ID	Error String
0	No_Err	OK (No error !)
1	DEV_ModName_Err	Getting the modules name error
2	DEV_ModNotExist_Err	The module doesn't exist in this port
3	DEV_PortNotOpen_Err	The port doesn't open
4	DEV_PortClose_Err	Closing i-7565-H1/H2 error
5	DEV_Reset_Err	Resetting i-7565-H1/H2 error
6	CAN_ConfigureFail_Err	CAN hardware configure fail
7	CAN_Hardware_Err	CAN hardware Init fail
8	CAN_PortNo_Err	The CAN port of Device over range
9	CAN_FIDLength_Err	The CAN Filter-ID number exceed max number
10	CAN_DevDisconnect_Err	The connection of device is broken
11	CAN_TimeOut_Err	The configure command is timeout
12	CAN_ConFigureCmd_Err	The configure command doesn't support
13	CAN_ConFigureBusy_Err	The configure command is busy
14	CAN_RxBufEmpty	The CAN receive buffer is empty
15	CAN_TxBufFull	The CAN send buffer is full
16	CAN_EnableHWCyclicTxMsg_Err	To enable hardware cyclic fail
17	CreateRxThread_Err	Creating Rx thread error
18	RestartRxThread_Err	Restarting Rx thread error

2.2 Function Descriptions

Table 2.2

NO	Init Function
1	VCI_OpenCAN
2	VCI_CloseCAN

No	Module Configure Function
1	VCI_Set_CANFID
2	VCI_Get_CANFID
3	VCI_Clr_CANFID
4	VCI_Get_CANStatus
5	VCI_Clr_BufOverflowLED
6	VCI_Get_MODInfo
7	VCI_Rst_MOD
NO	Communication Function
1	VCI_SendCANMsg
2	VCI_RecvCANMsg
3	VCI_EnableHWCyclicTxMsg
4	VCI_DisableHWCyclicTxMsg
NO	Software Buffer Function
1	VCI_Get_RxMsgCnt
2	VCI_Get_RxMsgBufIsFull
3	VCI_Clr_RxMsgBuf
NO	Other Function
1	VCI_Get_DllVer

2.3 Init FUNCTIONS

2.3.1 VCI_OpenCAN

- **Description:**
To enable the assigned CAN port function of I-7565-H1/H2. After the CAN port function is enabled, users can use “Communication” functions to send / receive CAN messages.
- **Syntax:**
int VCI_OpenCAN(PVCI_CAN_PARAM pCANPARAM)
- **Parameter:**
pCANPARAM:
A structure pointer of _VCI_CAN_PARAM is used to set the CAN port communication parameters shown as below.

```
typedef struct _VCI_CAN_PARAM
{
    BYTE DevPort;
```

```

    BYTE DevType;
    DWORD CAN1_Baud;
    DWORD CAN2_Baud;
} _VCI_CAN_PARAM, *PVCI_CAN_PARAM;

```

DevPort: The virtual com port number.

DevType: The module type (1: I-7565-H1; 2: I-7565-H2).

CAN1_Baud: CAN1 port baud rate.

(0: Disable CAN1 port Others: Enable CAN1 port)

CAN2_Baud: CAN2 port baud rate.

(0: Disable CAN2 port Others: Enable CAN2 port)

- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.3.2 VCI_CloseCAN

- **Description:**
To disable all CAN port function of I-7565-H1/H2. After the CAN port function is disabled, it will not interfere the communication of CAN bus network even if I-7565-H1/H2 is power on.
- **Syntax:**
int VCI_CloseCAN(PVCI_CAN_PARAM pCANPARAM)
- **Parameter:**
DevPort: The virtual com port number.
- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.4 Module ConFigureure FUNCTIONS

2.4.1 VCI_Set_CANFID

- **Description:**
To set CAN Filter-ID in the assigned CAN port.
- **Syntax:**
int VCI_Set_CANFID(BYTE DevPort, BYTE CAN_No, PVCI_CAN_FID pCANFID)
- **Parameter:**
DevPort: The i-7565-H1/H2 device index.
CAN_No : The assigned CAN port number.

pCANFID:

A structure pointer of _VCI_CAN_FilterID is used to set the CAN Filter-ID data shown as below.

```
typedef struct _VCI_CAN_FilterID
{
    WORD SSFF_Num;
    WORD GSFF_Num;
    WORD SEFF_Num;
    WORD GEFF_Num;
    WORD SSFF_FID[512];
    DWORD GSFF_FID[512];
    DWORD SEFF_FID[512];
    DWORD GEFF_FID[512];
} _VCI_CAN_FilterID, *PVCAN_CAN_FID;
```

SSFF_Num: Single 11-bit CAN Filter-ID number

GSFF_Num: Group 11-bit CAN Filter-ID number

SEFF_Num: Single 29-bit CAN Filter-ID number

GEFF_Num: Group 29-bit CAN Filter-ID number

SSFF_FID[512]: Single 11-bit CAN Filter-ID data array

GSFF_FID[512]: Group 11-bit CAN Filter-ID data array

- **Return:**

Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.4.2 VCI_Get_CANFID

- **Description :**

To get CAN Filter-ID in the assigned CAN port.

- **Syntax :**

```
int VCI_Get_CANFID(BYTE DevPort, BYTE CAN_No, PVCAN_CAN_FID
pCANFID)
```

- **Parameter :**

DevPort: The i-7565-H1/H2 device index.

CAN_No: The assigned CAN port number.

pCANFID:

A structure pointer of _VCI_CAN_FilterID is used to receive the CAN Filter-ID data shown as below.

```
typedef struct _VCI_CAN_FilterID
{
    WORD SSFF_Num;
    WORD GSFF_Num;
    WORD SEFF_Num;
```

```

WORD GEFF_Num;
WORD SSFF_FID[512];
DWORD GSFF_FID[512];
DWORD SEFF_FID[512];
DWORD GEFF_FID[512];
} _VCI_CAN_FilterID, *P_VCI_CAN_FID;

```

SSFF_Num: Single 11-bit CAN Filter-ID number
GSFF_Num: Group 11-bit CAN Filter-ID number
SEFF_Num: Single 29-bit CAN Filter-ID number
GEFF_Num: Group 29-bit CAN Filter-ID number
SSFF_FID[512]: Single 11-bit CAN Filter-ID data array
GSFF_FID[512]: Group 11-bit CAN Filter-ID data array
SEFF_FID[512]: Single 29-bit CAN Filter-ID data array
GEFF_FID[512]: Group 29-bit CAN Filter-ID data array

- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.4.3 VCI_Clr_CANFID

- **Description :**
To clear CAN Filter-ID in the assigned CAN port.
- **Syntax :**
int VCI_Clr_CANFID(BYTE DevPort, BYTE CAN_No)
- **Parameter :**
DevPort: The i-7565-H1/H2 device index.
CAN_No: The assigned CAN port number.
- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.4.4 VCI_Get_CANStatus

- **Description :**
To get the assigned CAN port status
- **Syntax :**
int VCI_Get_CANStatus(BYTE DevPort, BYTE CAN_No, P_VCI_CAN_STATUS pCANStatus)
- **Parameter :**

DevPort: The i-7565-H1/H2 device index.
CAN_No: The assigned CAN port number.
pCANStatus:
A structure pointer of `_VCI_CAN_STATUS` is used to receive the CAN port status shown as below.

```
typedef struct _VCI_CAN_STATUS
{
    DWORD CurCANBaud;
    BYTE CANReg;
    BYTE CANTxErrCnt;
    BYTE CANRxErrCnt;
    BYTE MODState;
    DWORD Reserved;
} _VCI_CAN_STATUS, *PVCI_CAN_STATUS;
```

CurCANBaud: Return the assigned CAN port baud rate.
CANReg: Return the assigned CAN port register value.
CANTxErrCnt : Return the assigned CAN port Tx error count.
CANRxErrCnt : Return the assigned CAN port Rx error count.
MODState : Return the module state.

- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.4.5 VCI_Clr_BufOverflowLED

- **Description :**
To clear buffer overflow ERR LED state (flash per second) in the assigned CAN port.
- **Syntax :**
`int VCI_Clr_BufOverflowLED(BYTE DevPort, BYTE CAN_No)`
- **Parameter :**
DevPort: The i-7565-H1/H2 device index.
CAN_No: The assigned CAN port number.
- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.4.6 VCI_Get_MODInfo

- **Description :**
To get the information of module.
- **Syntax :**
int VCI_Get_MODInfo(BYTE DevPort, PVCI_MOD_INFO pMODInfo)
- **Parameter :**
DevPort: The i-7565-H1/H2 device index.
pMODInfo:
A structure pointer of _VCI_MODULE_INFO is used to receive the module information shown as below.

```
typedef struct _VCI_MODULE_INFO
{
    char Mod_ID[12];
    char FW_Ver[12];
} _VCI_MODULE_INFO, *PVCI_MOD_INFO;
```

Mod_ID[12]: Return the module name string.
FW_Ver[12]: Return the module firmware version string.

- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.4.7 VCO_Rst_MOD

- **Description :**
To reset module.
- **Syntax :**
int VCI_Rst_MOD(PVCI_CAN_PARAM pCANPARAM)
- **Parameter :**
None.
- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.5 Communication FUNCTIONS

2.5.1 VCI_SendCANMsg

- **Description :**
To send CAN messages in the assigned CAN port.
- **Syntax :**
int VCI_SendCANMsg(BYTE DevPort, BYTE CAN_No, P_VCI_CAN_MSG pCANMsg)
- **Parameter :**
DevPort: The i-7565-H1/H2 device index.
CAN_No: The assigned CAN port number.
pCANMsg:
A structure pointer of _VCI_CAN_MSG is used to set the CAN message parameters shown as below.

```
typedef struct _VCI_CAN_MSG
{
    BYTE Mode;
    BYTE RTR;
    BYTE DLC;
    BYTE Reserved;
    DWORD ID;
    DWORD TimeL;
    DWORD TimeH;
    BYTE Data[8];
} _VCI_CAN_MSG, *P_VCI_CAN_MSG;
```

Mode: CAN message Mode (0: 11-bit; 1: 29-bit).
RTR: CAN message RTR (0: No RTR; 1: RTR).
DLC: CAN message Data Length (0~8).
ID: CAN message ID.
TimeL: CAN message Time-Stamp (Lo-DWORD).
TimeH: CAN message Time-Stamp (Hi-DWORD).
Data[8]: CAN message Data Array.

- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.5.2 VCI_RecvCANMsg

- **Description :**
To receive CAN messages that are saved in software buffer in the assigned CAN port.

- **Syntax :**
int VCI_RecvCANMsg(BYTE DevPort, BYTE CAN_No,
PVC_CAN_MSG pCANMsg)
- **Parameter :**
DevPort: The i-7565-H1/H2 device index.
CAN_No: The assigned CAN port number.
pCANMsg:
A structure pointer of _VCI_CAN_MSG is used to receive the CAN message shown as below.

```
typedef struct _VCI_CAN_MSG
{
    BYTE Mode;
    BYTE RTR;
    BYTE DLC;
    BYTE Reserved;
    DWORD ID;
    DWORD TimeL;
    DWORD TimeH;
    BYTE Data[8];
} _VCI_CAN_MSG, *PVC_CAN_MSG;
```

Mode: CAN message Mode (0: 11-bit; 1: 29-bit).
RTR: CAN message RTR (0: No RTR; 1: RTR).
DLC: CAN message Data Length (0~8).
ID: CAN message ID.
TimeL: CAN message Time-Stamp (Lo-DWORD).
TimeH: CAN message Time-Stamp (Hi-DWORD).
Data[8]: CAN message Data Array.

- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.5.3 VCI_EnableHWCyclicTxMsg

- **Description :**
To send CAN messages in the assigned CAN port by using module hardware timer and it will be more precise than PC software timer.
- **Syntax :**
int VCI_EnableHWCyclicTxMsg(BYTE DevPort, BYTE CAN_No,
PVC_CAN_MSG pCANMsg, DWORD TimePeriod, DWORD
TransmitTimes)

- **Parameter :**

DevPort: The i-7565-H1/H2 device index.

CAN_No: The assigned CAN port number.

pCANMsg:

A structure pointer of `_VCI_CAN_MSG` is used to set the CAN message parameters shown as below.

```
typedef struct _VCI_CAN_MSG
{
    BYTE Mode;
    BYTE RTR;
    BYTE DLC;
    BYTE Reserved;
    DWORD ID;
    DWORD TimeL;
    DWORD TimeH;
    BYTE Data[8];
} _VCI_CAN_MSG, *PVCI_CAN_MSG;
```

Mode: CAN message Mode (0: 11-bit; 1: 29-bit).

RTR: CAN message RTR (0: No RTR; 1: RTR).

DLC: CAN message Data Length (0~8).

ID: CAN message ID.

TimeL: CAN message Time-Stamp (Lo-DWORD).

TimeH: CAN message Time-Stamp (Hi-DWORD).

Data[8]: CAN message Data Array.

TimePeriod: The time period of module hardware timer for sending CAN message. If the value is zero, this function doesn't work.

TransmitTimes: The count for sending CAN message. If the value is zero, it means that CAN message will be sent periodically and permanently.

- **Return:**

Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.5.4 VCI_DisableHWCyclicTxMsg

- **Description :**

To stop sending CAN messages by module hardware timer.

- **Syntax :**

`int VCI_DisableHWCyclicTxMsg(PVCI_CAN_PARAM pCANPARAM);`

- **Parameter :**
pCANPARAM:
A structure pointer of _VCI_CAN_PARAM is used to set the CAN port communication parameters shown as below.

```
typedef struct _VCI_CAN_PARAM
{
    BYTE DevPort;
    BYTE DevType;
    DWORD CAN1_Baud;
    DWORD CAN2_Baud;
} _VCI_CAN_PARAM, *PVCI_CAN_PARAM;
```

DevPort: The virtual com port number

DevType: The module type (1: I-7565-H1; 2: I-7565-H2)

CAN1_Baud: CAN1 port baud rate

(0 : Disable CAN1 port Others: Enable CAN1 port)

CAN2_Baud: CAN2 port baud rate

(0 : Disable CAN2 port Others: Enable CAN2 port)

- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.6 Software Buffer FUNCTIONS

2.6.1 VCI_Get_RxMsgCnt

- **Description :**
To get the count of these received CAN messages saved in software buffer that are not received by users' program in the assigned CAN port.
- **Syntax :**
int VCI_Get_RxMsgCnt(BYTE DevPort, BYTE CAN_No, DWORD* RxMsgCnt)
- **Parameter :**
DevPort: The i-7565-H1/H2 device index.
CAN_No: The assigned CAN port number.
RxMsgCnt: The pointer is used to receive the CAN message count saved in software buffer.
- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.6.2 VCI_Get_RxMsgBufIsFull

- **Description :**
To get the software buffer state whether it is full or not in the assigned CAN port. If the software buffer is full, it means that some CAN messages are lost.
- **Syntax :**
int VCI_Get_RxMsgBufIsFull(BYTE DevPort, BYTE CAN_No, BYTE* Flag)
- **Parameter :**
DevPort: The i-7565-H1/H2 device index.
CAN_No: The assigned CAN port number.
Flag: The pointer is used to receive the state of software buffer. If the value is zero, the software buffer is not full. If not, it means that the software buffer is full.
- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.6.3 VCI_Clr_RxMsgBuf

- **Description :**
To clear the software buffer in the assigned CAN port.
- **Syntax :**
int VCI_Clr_RxMsgBuf(BYTE DevPort, BYTE CAN_No)
- **Parameter :**
DevPort: The i-7565-H1/H2 device index.
CAN_No: The assigned CAN port number.
- **Return:**
Return 0 means success, others means failure (Please refer to "Section 2.1 Error Code").

2.7 Other FUNCTIONS

2.7.1 VCI_Get_Library_Version

- **Description :**
To get the version of VCI_CAN library.
- **Syntax :**
char * VCI_Get_Library_Version (void)
- **Parameter :**
None.
- **Return:**
Return the VCI_CAN library version.

3. i-7565-H1/H2 Demo Programs For Linux

All function of demo programs will not work normally if i-7565-H1/H2 linux driver would not be installed correctly.

Table 3.1

Directory Path	File Name	Description
Include	i7565H1H2.h	The header of i-7565-H1/H2 library.
Lib	libI7565H1H2.a	Static library for LinPAC-8x81(or x86 32bits Linux PC).
	libI7565H1H2.so.1.0	Shared library for LinPAC-8x81(or x86 32bits Linux PC).
	libI7565H1H2_64.a	Static library for LinPAC-8x81(or x86 64bits Linux PC).
	libI7565H1H2_64.so.1.0	Shared library for LinPAC-8x81(or x86 64bits Linux PC).
	libI7565H1H2_arm.a	The i-7565-H1/H2 library for LinPAC-8x41.
doc	i7565-Linux-Manual.pdf	The linux manual for i-7565-H1/H2.
examples	I7565H1H2.c	The i-7565-H1/H2 demo source code.
	i7565H1H2_.a	An execution for LinPAC-8x81(or x86 Linux PC).
	i7565H1H2_.so	An execution for LinPAC-8x81(or x86 Linux PC).
	i7565H1H2_arm	An execution for LinPAC-8x41.

3.1 Demo name “i7565H1H2_.a” & “i7565H1H2_.so”

The i7565H1H2_.a is link to libi7565H1H2.a, and i7565H1H2_.so is link to libi7565H1H2.so.1.0

3.2 Demo code “i7565H1H2_.a”

This i-7565-H1/H2 demo program had provided below capability. Please follow below step to operate i-7565-H1 module in linux system.

Step 1: To choose exist device file of i-7565 module. Please refer to figure 3-1.

Step 2: Display device file name and device module name you choose.

Please refer to figure 3-1.

Step 3: To choose the baud rate of i-7565 CAN port. Please refer to figure 3-1

```
[root@localhost examples]# ./i7565H1H2_.a
I7565-H1/H2 device name:
1  ttyUSB0
2  ttyUSB2
USB-Serial Device Name :1
You choose "ttyUSB0", it's a I7565H1 module.
Configure I-7565-H1/H2 CAN Port Baudrate
1  :    5 Kbps
2  :   10 Kbps
3  :   20 Kbps
4  :   40 Kbps
5  :   50 Kbps
6  :   80 Kbps
7  :  100 Kbps
8  :  125 Kbps
9  :  200 Kbps
10 :  250 Kbps
11 :  400 Kbps
12 :  500 Kbps
13 :  600 Kbps
14 :  800 Kbps
15 : 1000 Kbps
I-7565-H1 CAN1 baud(1~15):15
```

Figure 3-1

Step 4: After user initial i-7565-H1 module well, the demo would show all capability. Please refer to figure 3-2.

```
a. Get I-7565-H1/H2 CAN State:
b. Get I-7565-H1/H2 Module Information:
c. Set I-7565-H1/H2 CAN Filter ID:
d. Get I-7565-H1/H2 CAN Filter ID:
e. Clear I-7565-H1/H2 CAN Filter ID:
f. Clear I-7565-H1/H2 Overflow LED:
g. Reset I-7565-H1/H2 Module:
h. Send CAN Message:
i. Receive CAN Message:
j. Enable Hardware Cyclic Send CAN Message:
k. Disable Hardware Cyclic Send CAN Message:
l. Get I-7565-H1/H2 Rx Message Count:
m. Get I-7565-H1/H2 Rx Message Buffer State:
n. Clear I-7565-H1/H2 Rx Message Buffer:
p. Show All I-7565-H1/H2function:
q. Shutdown and exit:
```

Step 4: The demo show all capability option for i-7565 module.

Figure 3-2

Step 5: To choose option 'a', 'b' to get the information of i-7565-H1 module. Please refer to figure 3-3.

```
a. Get I-7565-H1/H2 CAN State:
b. Get I-7565-H1/H2 Module Information:
c. Set I-7565-H1/H2 CAN Filter ID:
d. Get I-7565-H1/H2 CAN Filter ID:
e. Clear I-7565-H1/H2 CAN Filter ID:
f. Clear I-7565-H1/H2 Overflow LED:
g. Reset I-7565-H1/H2 Module:
h. Send CAN Message:
i. Receive CAN Message:
j. Enable Hardware Cyclic Send CAN Message:
k. Disable Hardware Cyclic Send CAN Message:
l. Get I-7565-H1/H2 Rx Message Count:
m. Get I-7565-H1/H2 Rx Message Buffer State:
n. Clear I-7565-H1/H2 Rx Message Buffer:
p. Show All I-7565-H1/H2function:
q. Shutdown and exit:
a Step 5: To choose option 'a', 'b'
I-7565-H1/H2 CAN Port Status
CAN Port 1 : Baudrate 1000 K
CAN Port 1 : Error Count Tx 0 Rx 0
CAN Port 1 : Module State 0
b
Module ID : I-7565-H1
Module Firmware : v1.01
```

Option 'a' : To show the information of CAN port.

Option 'b' : To show the information of i-7565 module.

Figure 3-3

Step 6: To choose option 'c', 'd' and 'e' to configure the filter ID of i-7565-H1 module. Please refer to figure 3-4, 3-5.

```

c
Set CAN1 Filter ID OK
d
I-7565-H1/H2 CAN Port 1 Filter State
CAN1 11-bits Single Standard FID Number = 3
11-bits SFFF_FID : 0x0122
11-bits SFFF_FID : 0x0123
11-bits SFFF_FID : 0x0124

CAN1 11-bits Group Standard FID Number = 2
11-bits GSFF_FID : 0x0010 ~ 0x0020
11-bits GSFF_FID : 0x0030 ~ 0x0040

CAN1 29-bits Single Extended FID Number = 3
29-bits SEFF_FID : 0x00000011
29-bits SEFF_FID : 0x00000012
29-bits SEFF_FID : 0x00000013

CAN1 29-bits Group Extended FID Number = 2
29-bits GEFF_FID : 0x00000100 ~ 0x00000200
29-bits GEFF_FID : 0x00000300 ~ 0x00000400

```

Option 'c': To set the filter ID of i-7565-H1's CAN 1.

Option 'd': To get the filter ID of i-7565-H1's CAN 1.

Figure 3-4

```

e
CAN Port 1 : Clear Filter ID OK
d
I-7565-H1/H2 CAN Port 1 Filter State
CAN1 11-bits Single Standard FID Number = 0

CAN1 11-bits Group Standard FID Number = 0

CAN1 29-bits Single Extended FID Number = 0

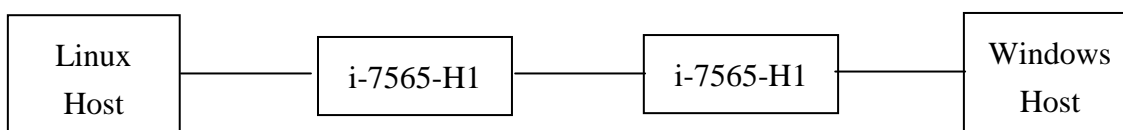
CAN1 29-bits Group Extended FID Number = 0

```

Option 'e': To clear Filter ID.

Figure 3-5

Step 7: Before choosing the option, user should build the test environment first. Please refer to below test environment.



To choose option 'h' to send a CAN message to another i-7565-H1 module that installing in Windows system. Please refer to figure 3-6, 3-7(Windows host use i-7565 utility to get CAN message).

```

h
Use Default CAN Message (y/n):n Option 'h': To send a CAN message from i-7565-H1
CAN Message ID :123 module that installing in linux system.
CAN Message Mode :0
CAN Message RTR :0
CAN Message Length :8
CAN Message Data[0] :1
CAN Message Data[1] :2
CAN Message Data[2] :3
CAN Message Data[3] :4
CAN Message Data[4] :5
CAN Message Data[5] :6
CAN Message Data[6] :7
CAN Message Data[7] :8
Send CAN Message(Mode 0 ID(Hex) 123 RTR 0 DLC 8 D1 1 D2 2 D3 3 D4 4 D5 5 D6 6 D7 7 D8 8
  
```

Figure 3-6

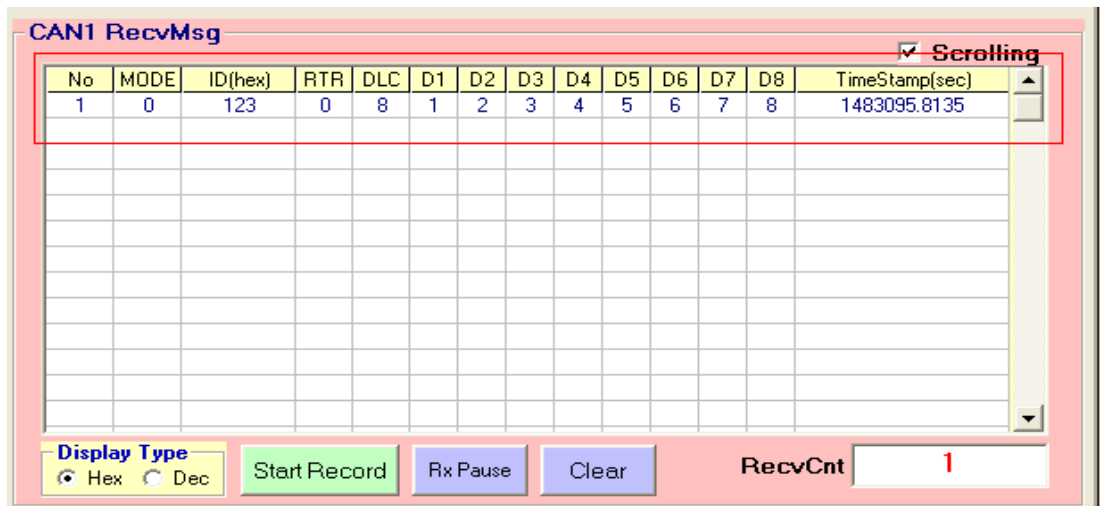
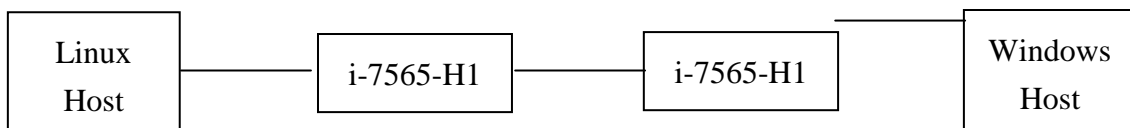


Figure 3-7

Step 8: Before choosing the option, user should build the test environment first. Please refer to below test environment.



To choose option 'i' to receive a CAN message from i-7565-H1 module that installing in Windows system. Please refer to figure 3-8(Windows host use i-7565 utility to send CAN message “Mode 0 ID(Hex) 110 RTR 0 DLC 8 D1 1 D2 2 D3 3 D4 4 D5 5 D6 6 D7 7 D8 8”), 3-9

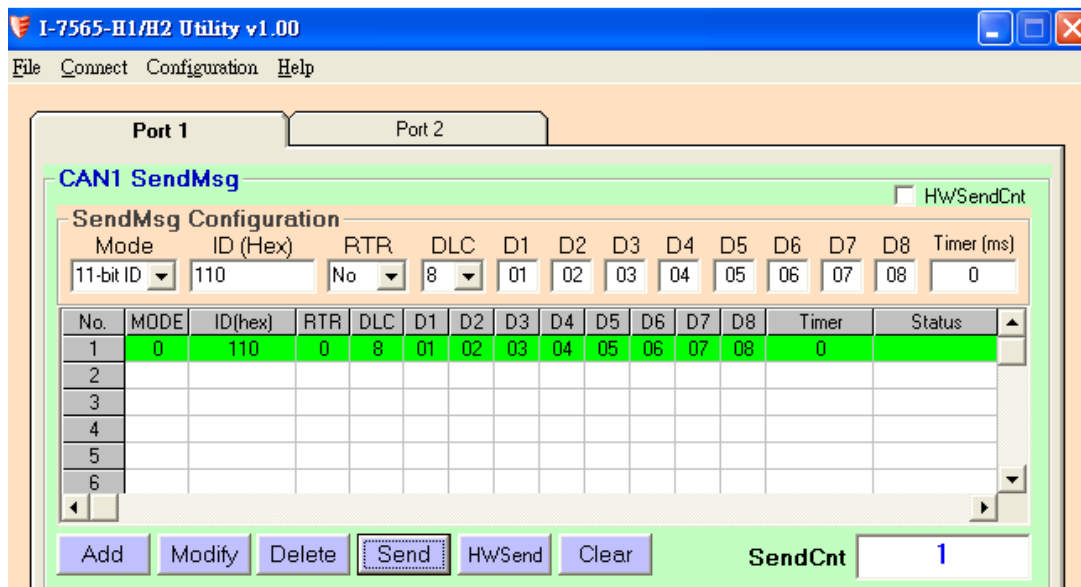
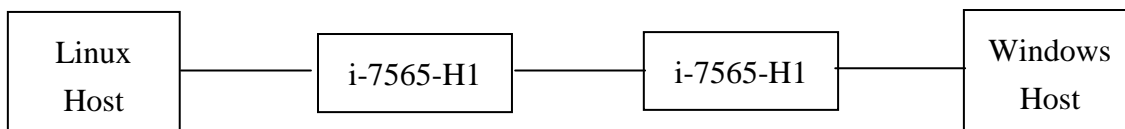


Figure 3-8



Figure 3-9

Step 9: Before choosing the option, user should build the test environment first. Please refer to below test environment.



To choose option 'j' to enable hardware timer to send CAN message. Please refer to figure 3-10, 3-11.


```

j
Use Default Config to Cyclic Send CAN Message (y/n):n
HW CAN Message Count :10
HW CAN Message Time Period(ms):100
CAN Message ID :120
CAN Message Mode :0
CAN Message RTR :0
CAN Message Length :8
CAN Message Data[0] :1
CAN Message Data[1] :2
CAN Message Data[2] :3
CAN Message Data[3] :4
CAN Message Data[4] :5
CAN Message Data[5] :6
CAN Message Data[6] :7
CAN Message Data[7] :8
Send CAN Message(Mode 0 ID(Hex) 120 RTR 0 DLC 8 D1 1 D2 2 D3 3 D4 4 D5 5 D6 6 D7 7 D8 8) OK

```

Option 'j': To enable HW timer to send 10 CAN message.

Figure 3-10

The screenshot shows the 'CAN1 RecvMsg' application window. It features a table with 13 columns: No, MODE, ID(hex), RTR, DLC, D1, D2, D3, D4, D5, D6, D7, D8, and TimeStamp(sec). The table contains 10 rows of data, all with MODE 0, ID 120, and RTR 0. The DLC is 8, and the data bytes (D1-D8) are 1 through 8 respectively. The timestamps range from 1485767.9575 to 1485768.8575. Below the table, there are controls for 'Display Type' (Hex selected), 'Start Record', 'Rx Pause', and 'Clear' buttons. A 'RecvCnt' field is highlighted with a red box and shows the value '10'. A 'Scrolling' checkbox is checked in the top right corner.

No	MODE	ID(hex)	RTR	DLC	D1	D2	D3	D4	D5	D6	D7	D8	TimeStamp(sec)
1	0	120	0	8	1	2	3	4	5	6	7	8	1485767.9575
2	0	120	0	8	1	2	3	4	5	6	7	8	1485768.0575
3	0	120	0	8	1	2	3	4	5	6	7	8	1485768.1575
4	0	120	0	8	1	2	3	4	5	6	7	8	1485768.2575
5	0	120	0	8	1	2	3	4	5	6	7	8	1485768.3575
6	0	120	0	8	1	2	3	4	5	6	7	8	1485768.4575
7	0	120	0	8	1	2	3	4	5	6	7	8	1485768.5575
8	0	120	0	8	1	2	3	4	5	6	7	8	1485768.6575
9	0	120	0	8	1	2	3	4	5	6	7	8	1485768.7575
10	0	120	0	8	1	2	3	4	5	6	7	8	1485768.8575

Figure 3-11

Step 10: To choose option 'q' to finish demo.