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# I-8172(H)/71(H)

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## User Manual

### Warranty

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# 1. INTRODUCTION

FRnet is a two-wire serial communication bus and is wired in a similar manner as the RS-485. FRnet devices connect to the communication by using the multi-drop method. Unlike most communication methods based on RS-485, FRnet does not use the traditional question/answer approach. Instead, it uses special chips to actively transmit data with a fixed scan time. Since there is no need of a CPU to process a communication protocol, FRnet can achieve high-speed data transmission in an isochronous manner.

The I-8172/72H/71/71H is an isolated FRnet communication module designed for use in WinCon, LinCon and I-8000 controllers. The I-8171 module has one FRnet port whereas the I-8172 module has two FRnet ports. Each FRnet port has 8 sender nodes and 8 receiver nodes. That is, the node address setting is defined as SA0~SA7 and RA8~RA15. Each node contains 16-bit data, which can be either a DI or DO type depending on what module you use. Therefore, it can control up to a maximum of 128 (= 16\*8) digital output channels and 128 (= 16\*8) digital input channels with a total scan time of 2.88ms for 250kbps or 0.72ms for 1Mbps (for the H version).

**Note: The H version modules can not work together with the normal speed modules.**

I/O data transmission of the FRnet communication is controlled by the FRnet control chip which was developed by ICPDAS. It is designed to provide a deterministic high speed communication for this network. This communication mechanism is dominated by the token stream, which is generated by the network manager (located at SA0 node). It provides a fixed scan-time for I/O synchronization without special communication protocol. Furthermore, special anti-noise circuitry has been built into the FRnet control chip to ensure communication reliability.

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However, the effectiveness of the FRnet depends on and is then ensured when the correct hardware configurations for the sender address (SA) and receiver address (RA) on the host controller and the remote module in the network have been installed properly. In general, the operating principle is structured by the strategy of delivering the 16-bit data from the specified sender address (SAn) to the corresponding receiver address (RAn) via the broadcasting method controlled by the token-stream of the network manger, SA0. Based on this algorithm, there are some general rules that need to be followed:

- (1) The sender address needs to be unique in order to avoid any communication collisions.
- (2) Each of FRnet bus needs one and only one network manger defined as SA0. It plays the important role of producing the token-stream in the network.
- (3) The baud rates of the controller and the remote module need to be the same as on the FRnet.
- (4) The communication method is controlled by delivering the data of the specified sender address (SA) to the corresponding receiver address (RA) in the sequence of token 0 to (N-1) cyclically, as depicted below.
- (5) Due to the broadcasting algorithm adopted, the receiver address is not required to be unique. Therefore, it is easy to build in data delivery from one node (16-bit data) to a multi-node.

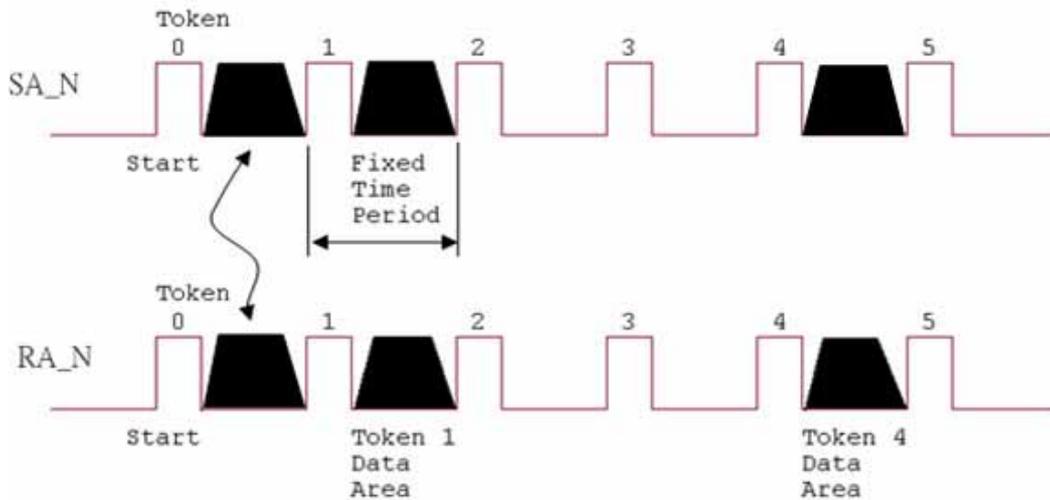


Figure 1.1 Token stream controlled by network manager, SA0

Each port of I-8172/71 host module contains nodes from SA0~7 and RA8~15. That means each port on I-8172/71 can connect to devices which have node addresses set between RA0~7 and SA8~15. The RA node on device can receive the command data from host module. Therefore, remote FRnet DO modules must have RA nodes. In a similar way, the remote FRnet DI modules must have SA nodes. The registers of SA and RA nodes on the host are mapped to a region of host's memory. Users can easily control the FRnet I/O module through reading and writing the specified memory located on the host computer.

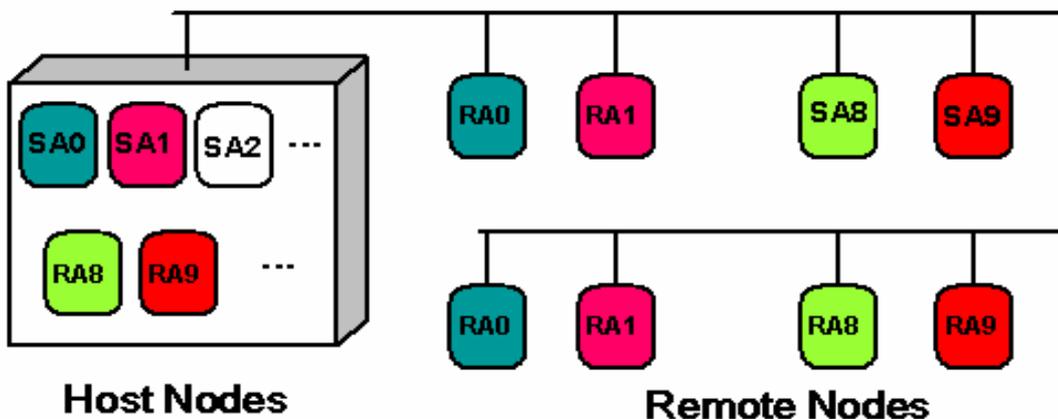


Figure 1.2 FRnet application structure

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## 1.1 Features

- The Token stream is used to activate data transmission from the specified SA node to the corresponding RA nodes.
- The Network Manager is defined as SA0. Each of FRnet must have SA0 because it issues the Token stream into the network.
- The Token stream is produced cyclically by hardware at the fixed time interval, see Figure 1.1. Therefore, the FRnet I/O system can provide both the isochronous and deterministic functionalities.
- It can provide data transmission from one node (SA) to remote multiple nodes (RA) at the same time. It implies that the SA address must be unique, but the RA address can be different or the same in the network.
- The FRnet system can be easily extended by adding new modules to the network.
- Device Inter-communication: A single device can talk to other devices by setting appropriate SA and RA node configurations.
- Adopt Memory-mapping technology to control I/O nodes.
- No software overhead: all data transmissions are performed automatically via the FRnet control chips. Therefore, there is no need for the CPU to process transmission protocols.
- It only needs simple RS-485 wiring method.
- OS (operation system) independent.

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## 1.2 Specifications

Table 1.1 Characteristics of the I-8172

	I-8172/71	I-8172H/71H
Transfer speed	250Kbps	1Mbps
Scan time	2.88ms	0.72ms
Max transfer length	400 m	100 m

Table 1.2 I/O Address of I-8172

	I-8172/H	I-8171/H
I/O Address for Port 0	SA [0] ~ [7] , RA [8] ~ [15]	SA [0] ~ [7] , RA [8] ~ [15]
I/O Address for Port 1	SA [0] ~ [7] , RA [8] ~ [15]	Not Available

Note: SA: Sender Address of a node, RA: Receiver Address of a node.

### General specifications:

- Operation temperature: -25°C~+75°C
- Storage temperature: -30°C~+80°C
- Humidity: 5~95%
- Power consumption: 2 W(Max)

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## 1.3 Ordering information

- I-8172 : 250Kbps (2 Ports: SA 0~7, RA 8~15)
- I-8171 : 250Kbps (1 Port : SA 0~7, RA 8~15)
- I-8172H: 1Mbps (2 Ports: SA 0~7, RA 8~15)
- I-8171H: 1Mbps (1 Port : SA 0~7, RA 8~15)

Note: the high-speed version can be selected by an internal jumper.

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### 1.3.1 Options

- FR-2057 series: 16-channel Isolated Digital Output Distributed I/O Module.
- FR-2053 series: 16-channel Isolated Digital Input Distributed I/O Module.
- DN-20/1m: Terminal board with two 20-pin flat cables (CA2010).
- FR-8R/16R/32R: 8/16/32-channel relay output terminal board.
- FR-8P/16P/32P: 8/16/32-channel photo-isolated input terminal board.
- FR-8A/16A/32A: 8/16/32-channel Open drain output terminal board.

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## **1.4 Product Check list**

**In addition to this manual, this package should include the following items:**

- **One I-8172/71 module**
- **One copy of the release notes**

**Before continuing, please read the release notes first. They contain the following important information.**

- 1. The location of the software driver and utility**
- 2. FAQ's**

### **Attention!**

**If any of these items are missing or damaged, contact the dealer from whom you purchased the product. Save all shipping materials and the carton in case you need to ship or store the product in the future.**

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## 2. HARDWARE CONFIGURATION

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### 2.1 Internal I/O Structure

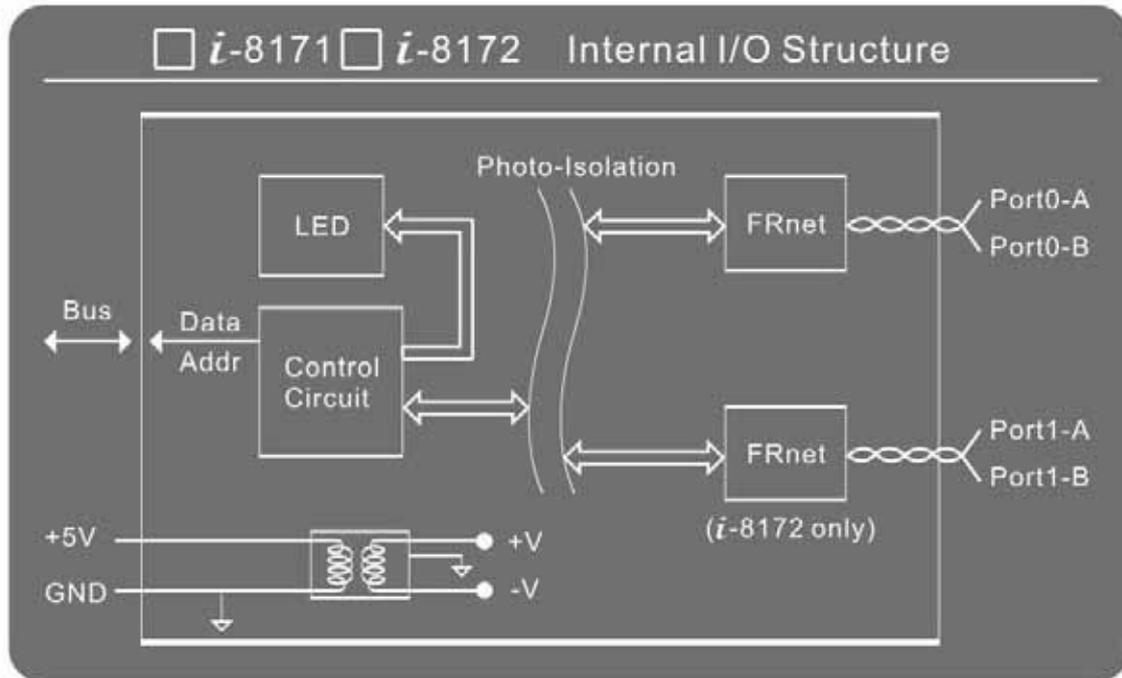
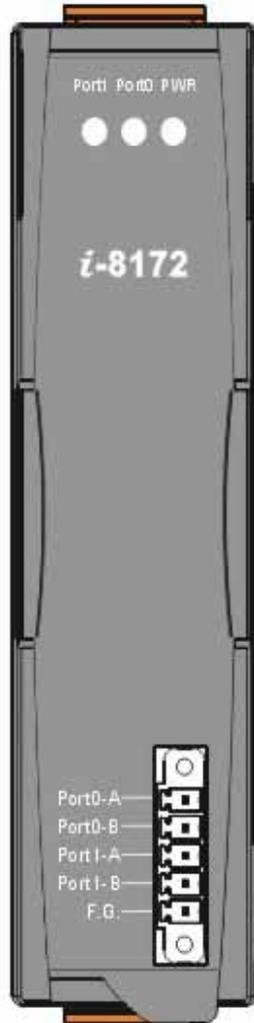


Figure 2.1 Function blocks of the I-8172/71

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## 2.2 Pin Assignments

The I-8172 module has two FRnet ports (similar to RS-485 ports). The definitions of the pins on the connectors for the FRnet ports are shown in the following Table.



Terminal No.	Pin Assignment Name
01	Port0-A
02	Port0-B
03	Port1-A
04	Port1-B
05	F.G.

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## 3. SOFTWARE FUNCTIONS

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### 3.1 I-8000 controllers

For BC and TC users, please use `i8172_Init()`, `i8172_Write_DO_Group()` and `i8172_Read_DI_Group()` function to control the FRnet DI/O. The necessary step and function description are as follows.

1. Add “#Include “i8172.h” to the header of project.
2. Function Lists:

**int i8172\_Init (int iSlot);**

/\*\*\*\*\*\*

Read hardware ID

User can use this function to determine HW ID.

Slot: 0 ~ 7

return: 0 OK

-1 Error

\*\*\*\*\*/

**void i8172\_Write\_DO\_Group (int iSlot, int iPort, int iGroup, unsigned int iData);**

/\*\*\*\*\*\*

User can use this function to write value to DO module.

Slot: 0 ~ 7

Port: 0 or 1

Group: 0 ~ 7

Every group has 16 channels.

\*\*\*\*\*/

**void i8172\_Write\_DO\_Bit(int iSlot, int iPort, int iGroup, int iBit, int iStatus);**

/\*\*\*\*\*\*

User can use this function to input single channel to DO module.

Slot: 0 ~ 7

Port: 0 or 1

Group: 0 ~ 7

---

**iBit:**            Every group has 16 channels.  
                  1~16  
**iStatus:**        **0**        **off**  
                  **1**        **on**

\*\*\*\*\*/

**unsigned int i8172\_Read\_DI\_Group (int iSlot, int iPort, int iGroup);**  
/\*\*\*\*\*

User can use this function to read value from DI module.

**Slot:**            **0 ~ 7**  
**Port:**            **0 or 1**  
**Group:**           **0 ~ 7**  
                  Every group has 16 channels.

\*\*\*\*\*/

**unsigned int i8172\_Read\_DI\_Bit (int iSlot, int iPort, int iGroup, int iBit);**  
/\*\*\*\*\*

User can use this function to read single channel from DI module.

**Slot:**            **0 ~ 7**  
**Port:**            **0 or 1**  
**Group:**           **0 ~ 7**  
                  Every group has 16 channels.  
**iBit:**            **1 ~ 16**

\*\*\*\*\*/

**int i8172\_GetLibVersion ( );**

/\*\*\*\*\*

User can use this function to read the version of lib.

\*\*\*\*\*/

**void i8172\_GetLibDate (unsigned char \*LibDate);**

/\*\*\*\*\*

User can read the released date of lib.

\*\*\*\*\*/

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## 3.2 WinCon-8000 controllers

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For eVC++ 4.0 user only use FRNET\_SA() and FRNET\_RA() function can control the FRNET DI/O. The necessary step and function description are as follows.

1. Added “#Include “WinConSDK.h” to the header of project.
2. In your application, use FRNET\_SA() for DO output.

FRNET\_SA (BYTE Slot, BYTE Port, BYTE wSA, WORD Data);

For example:

```
FRNET_SA(1, 0, 0, 0x3);
```

```
//Write 0x3 to SA 0 of FRnet port 0 on the host module located at Slot 1. This  
//value will be sent to remote FRnet RA module (DO) and set the bit 0 and 1 to  
//be active (On).
```

4. In your application, use FRNET\_RA() for DI input.

FRNET\_RA (BYTE Slot, BYTE Port, BYTE wRA);

For example:

```
WORD Read_BK = FRNET_RA (1, 0, 8);
```

```
//Get the DI status from RA 8 of FRnet port 0 on the host module located at  
//Slot 1. The value is saved to the Read_BK variable.
```

**NOTE: For more detailed information, please refer to the manual of WinCon-8000 ( WinCON EVC++ SDK).**

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## 5. FRnet APPLICATION STRUCTURE

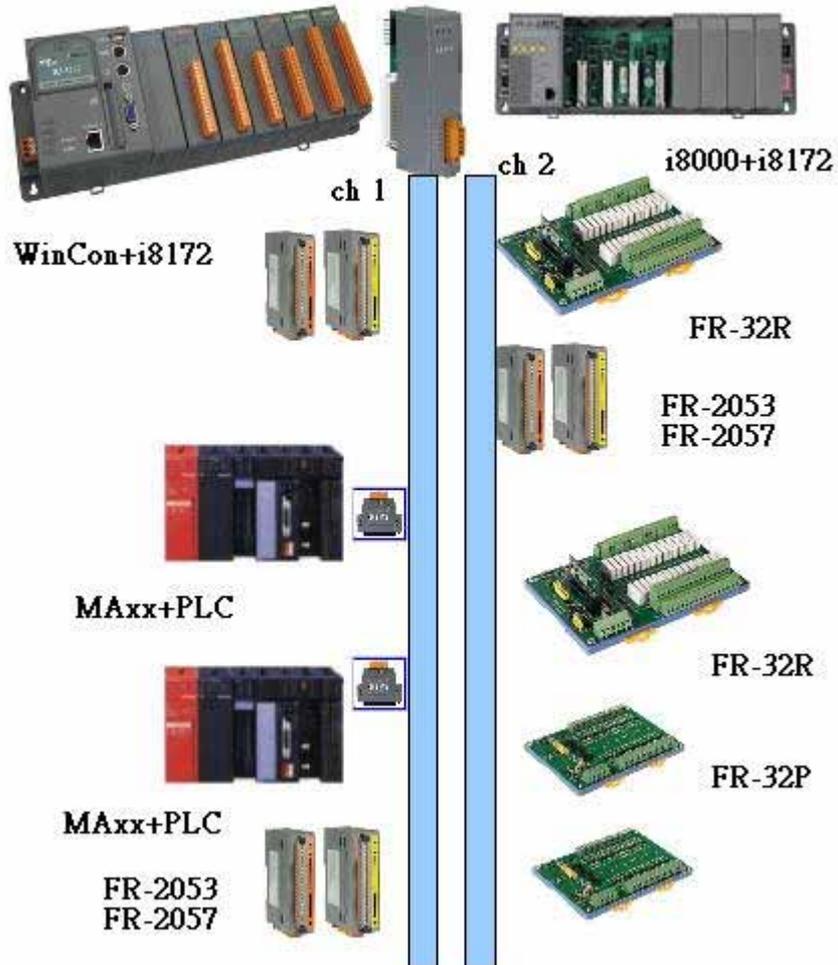


Figure 4.1

**Note:**

1. Refer to the “FRnet distributed I/O module manual” for details regarding the settings of the DSW (dipswitch).
2. The high-speed FRnet cards and modules can only work together with high-speed remote modules. Similarly, normal speed FRB cards can only work with normal speed remote modules.
3. The cabling method is similar to that used with the RS-485 networks. For long distance usage, a shielded twisted pair cable is required.